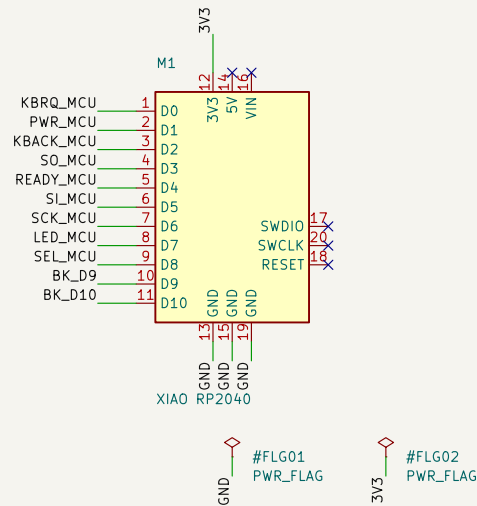
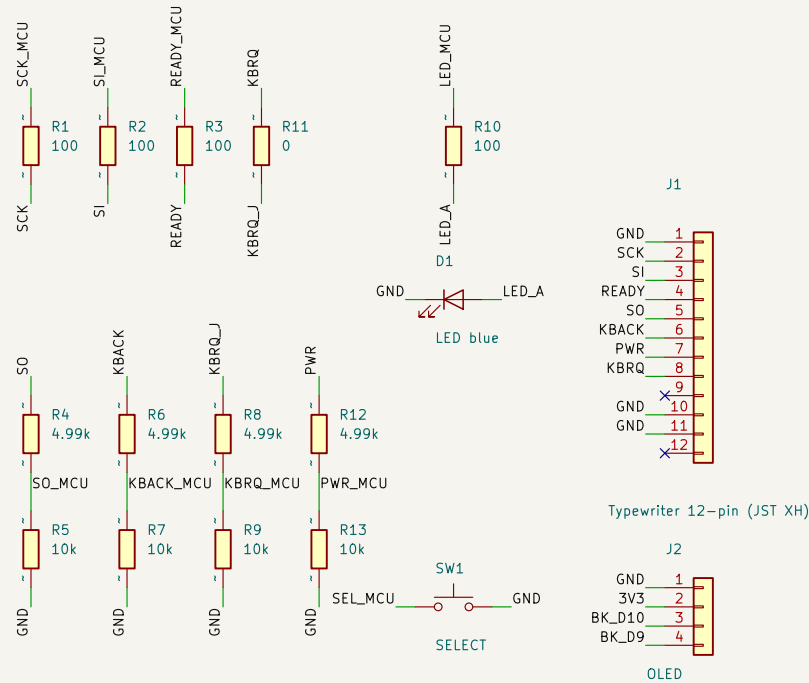




Outputs XIAO -> typewriter: SCK (D6), SI (D5), READY (D4) through 100 R. 3.3 V is a valid high for the typewriter's 7407 inputs.
 Inputs typewriter -> XIAO: SO (D3), KBACK (D2), KBRQ (D0), +5 V power sense (D1), each through a 4.99k/10k divider (5 V -> 3.34 V).
 R11 = 0 R jumper (lets KBRQ cross the ground trace on the single-sided board). LED D7 = ONLINE lamp, SW1 D8 = SELECT (space bar).



J1 pin order is PROVISIONAL (chosen for routing): 1 GND 2 SCK 3 SI 4 READY 5 SO 6 KBACK 7 +5V sense 8 KBRQ 9 nc 10-11 GND 12 nc.

Plug pins 11/12 carry +30 V and are never wired. Re-pin the harness housing to match after the continuity map.

J2 = OLED socket in the standard 0.96in order GND VDD SCL(D10) SDA(D9): a module plugs in directly, or 4 wires to the case lid.

ChronoType IF-60 replacement, HTMAA 2026 week 5. Bus spec: Brother CE-650 service manual ch. 9 + LooksLikeThomas/brother-serial-interface-adapter.

Fadi G. Haddad, MIT HTMAA 2026

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File: chronotype_typewriter.kicad_sch

Title: ChronoType IF-60 replacement (typewriter board)

Size: A4 Date: 2026-10-13

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Rev: 0.3

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